



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Applicant  
Application No.  
Filed  
For

: Ubaldo Mastromatteo  
: 10/037,485  
: December 19, 2001  
: PROCESS FOR MANUFACTURING INTEGRATED DEVICES  
: HAVING CONNECTIONS ON A SEPARATE WAFER, AND  
: INTEGRATED DEVICE THUS OBTAINED

*Handwritten:* #9/B  
4/22/03  
*Signature:* [Signature]

Examiner : Hung K. Vu  
Art Unit : 2811  
Docket No. : 854063.665  
Date : March 4, 2003

Commissioner for Patents  
Washington, DC 20231

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AMENDMENT

Commissioner for Patents:

In response to the Office Action dated December 4, 2002, please amend the application as follows:

In the Claims:

For the convenience of the Examiner, all pending claims have been included.  
Please amend claims 1, 4, 5, 11, and 30 to read as follows:

*Handwritten:* Baber

1. (Amended) A process for manufacturing an integrated device, comprising:  
forming integrated structures including semiconductor regions and isolation regions in a first wafer of semiconductor material;